

FORM PTO-1449		U. S DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. <b>U 015201-9</b>	SERIAL NO. <b>10/849,491</b>
<div style="display: flex; align-items: center;"> <div> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(Use several sheets if necessary)</p> </div> </div>				APPLICANT	
				Lalit M. BHARADWAJ, et al.	
				FILING DATE	GROUP
				MAY 19, 2004	
U.S. PATENT DOCUMENTS					
EXAMINER INITIALS	REFERENCE DESIGNATION	DOCUMENT NUMBER	DATE	NAME	FILING DATE IF APPROPRIATE
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
FOREIGN PATENT DOCUMENTS					
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION
					YES      NO
/JSB/	AL	2 376 686	12/2002	GB	
	AM				
	AN				
	AO				
	AP				
OTHER ART (Including Author, Title, Date, Pertinent Dates, Etc.)					
/JSB/	AR	Baer, J. "Computer Systems Architecture" Chapter 3-Arithmetic Algorithms <i>Computer Science Press</i> (1980) p 76-86, p 118-123, XP002314943			
/JSB/	AS	Awwal, A.A.S., et al. "Fast Carry Free Adder Design Using QSD Number System" <i>Aerospace and Electronics Conference</i> (1993) pp 1085-1090			
	AT				
EXAMINER		/John S. Brusca/		DATE CONSIDERED    04/27/2007	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					